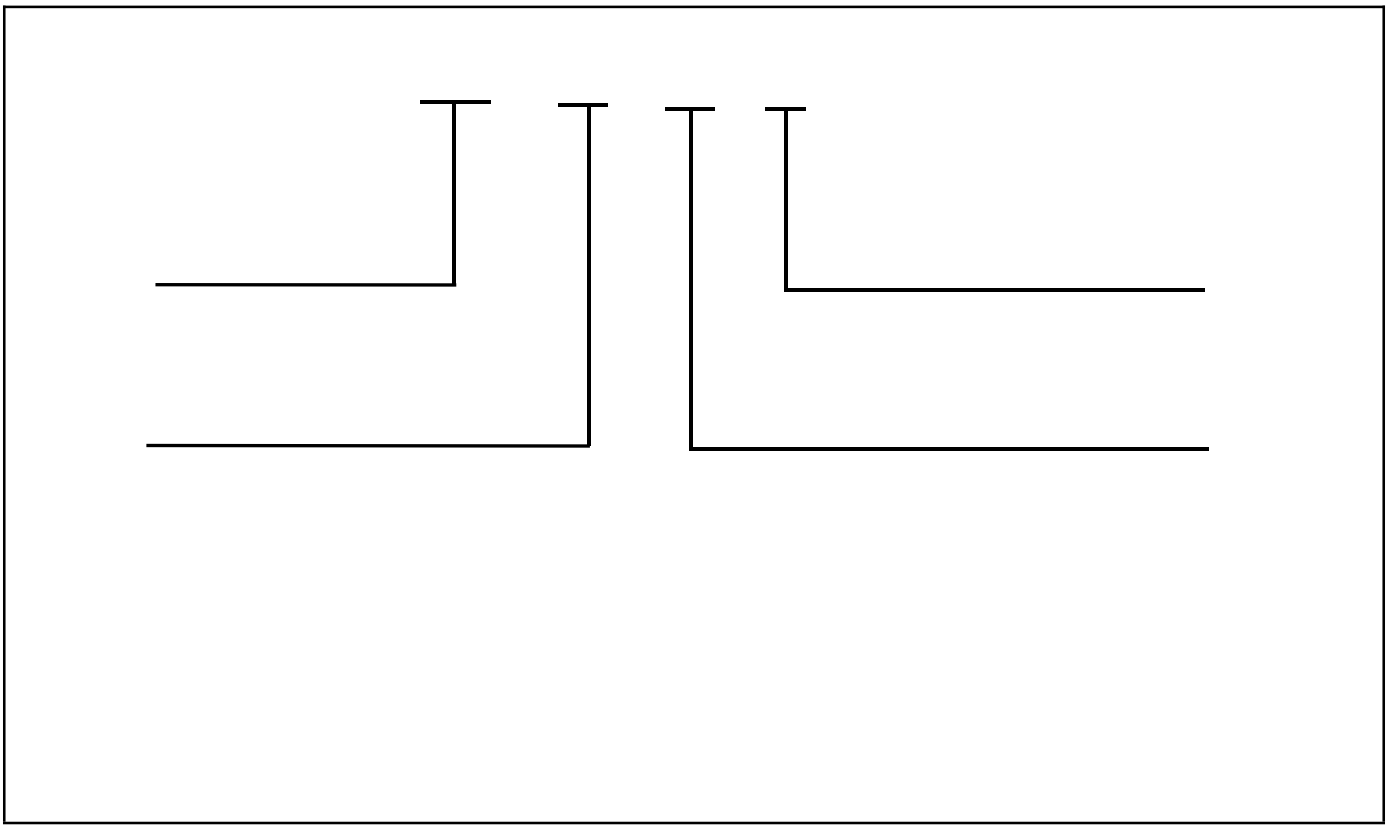
				E		
Gate trigger current		V_D = 12 V,R_L=100Ω	- -	≤50		
				≤120		
Gate trigger voltage			MAX	1.5		
Non-triggering gate voltage		V_D=V_{DRM} T_j=125	MIN	0.2		
Holding current		I_T=0.5A	MAX	60		
Latching current		I_G=1.2I_{GT}	-	60		
				100		
Critical-rate of rise of commutation voltage		V_D= %V_{DRM} , T_j=125		500		
On-state Voltage		I_{TM}=32A,		1.5		
Repetitive Peak Off-State Current		V_D=V_{DRM}= V_{RRM}	T_j=25	5		
Repetitive Peak Reverse Current			T_j=125	1		
Ihermal resistance		Junction to case		TYP.	1.3	
		Junction to ambient		TYP.	60	



Typi

