Static Characteristics T_j = 25 °C unless otherwise stated

Parameter	Conditions		Symbol	Min	Typ	Max	Unit
							V/μs
							V/μs

Typical Characteristics

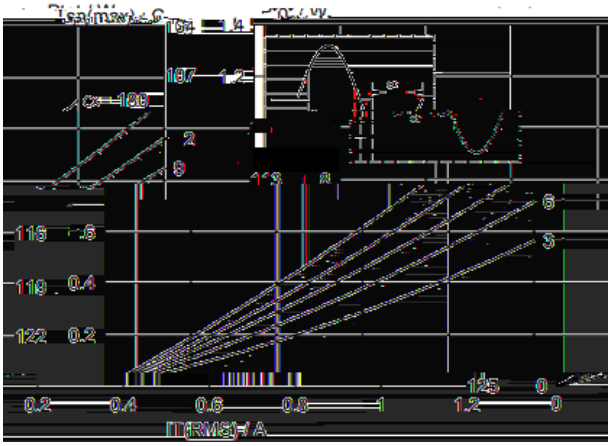

Fig.1. Maximum on-state dissipation, Ptot, versus rms on-state current, $I_{T(RMS)}$,

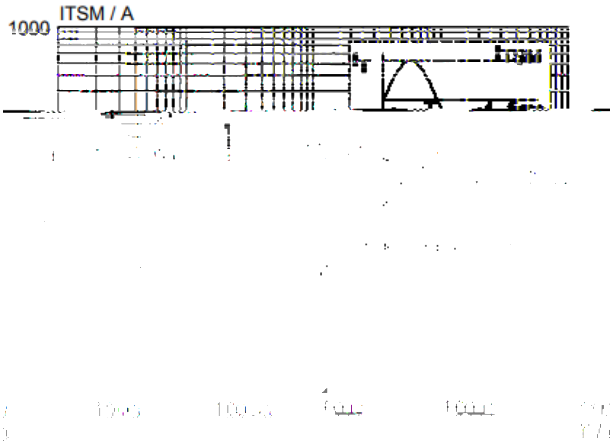

Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM}, versus pulse width

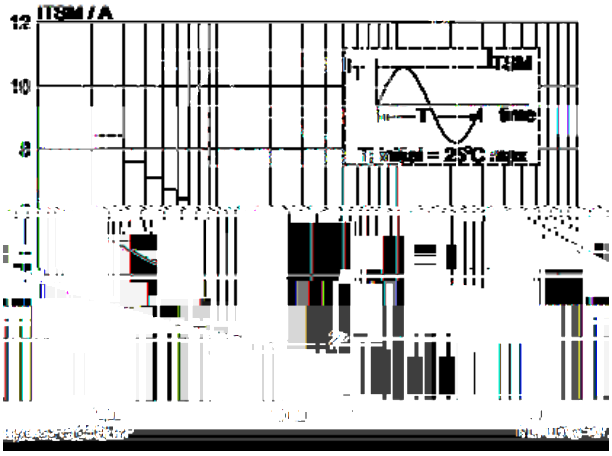

Fig.3. Maximum permissible non-repetitive peak on-state current I_{TSM}, versus number of cycles, for sinusoidal currents, f = 50 Hz.

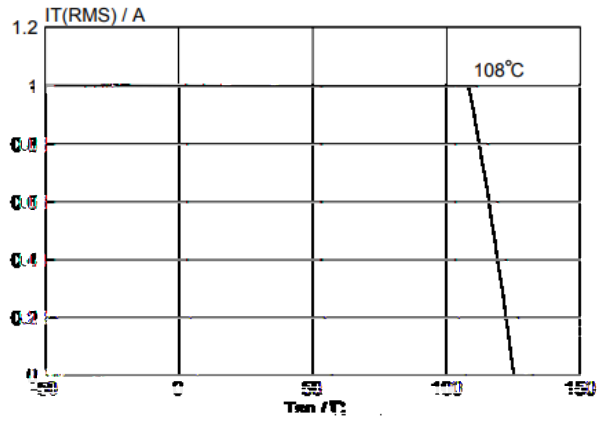

Fig.4. Maximum permissible rms current $I_{T(RMS)}$, versus solder point temperature Tsp.

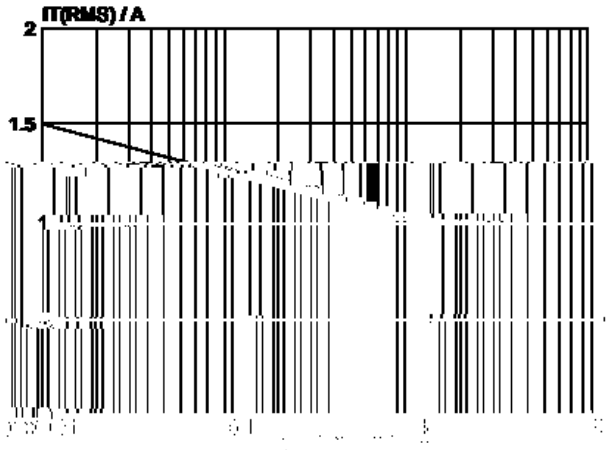

Fig.5. Maximum ermissible repetitive rms n-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50Hz; T_{sp}

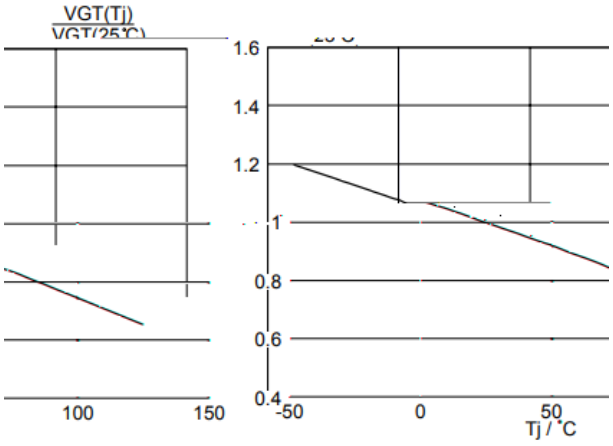

Fig.6.Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(\quad)$, versus junction temperature T_j

Typical Characteristics

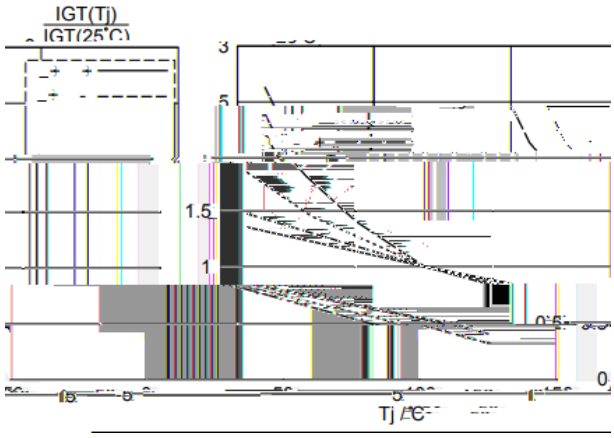

Fig.7. Normalised gate trigger current $I_{GT}(T_j)$/ I_{GT} $_j$.

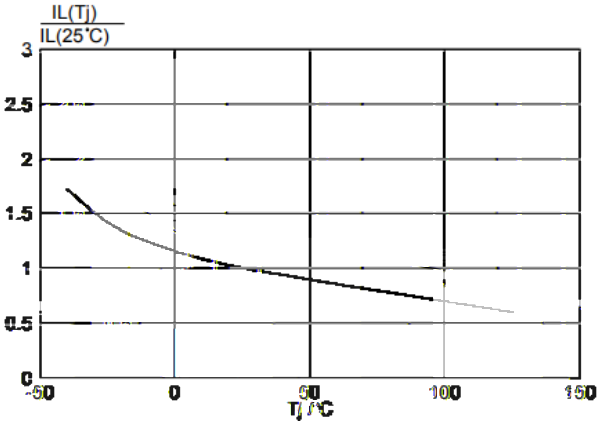

Fig.8. Normalised latching current $I_L(T_j)$/ I_L $_j$.

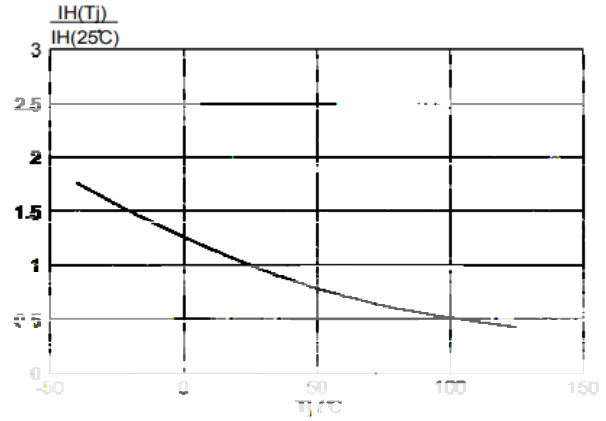

Fig.9. Normalised holding current $I_H(T_j)$/ I_H $_j$.

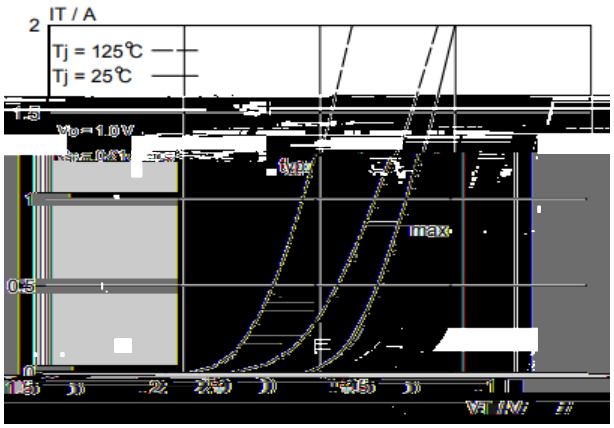

Fig.10. Typical and maximum on-state characteristic.

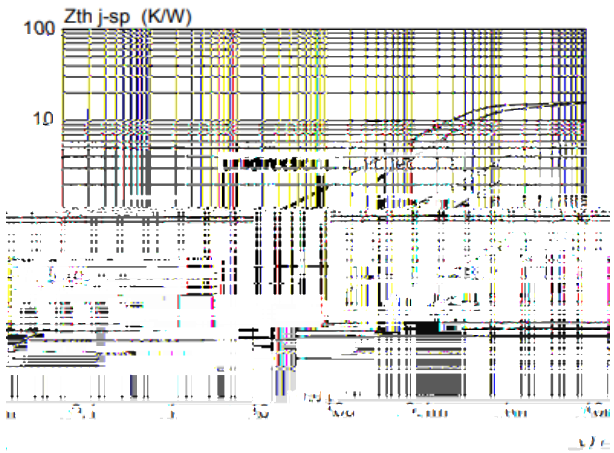

Fig.11. Transient thermal impedance $Z_{th\ j\text{-}sp}$, versus pulse width tp.

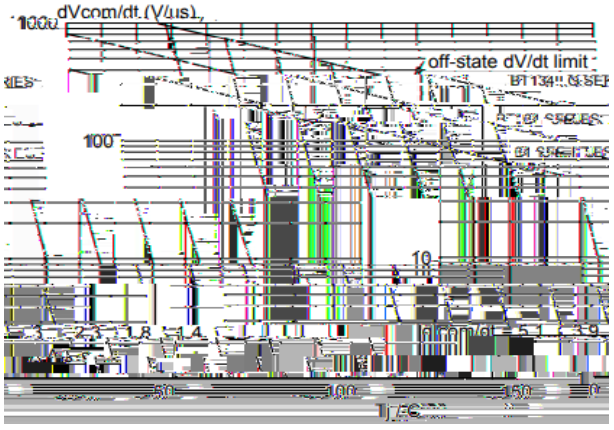

Fig.12. Typical commutation dV/dt versus junction temperature, parameter commutation dI_T/dt. The triac should commutate when the dV/dt is below the value on the appropriate curve for pre-commutation dI_T/dt.

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